

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	internal adj ADC adj clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:39
L2	439	ADC adj clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:39
L3	1	ADC adj clock same cycle adj length	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:39
L4	3	ADC adj clock same dll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:40
L5	4	cycle adj length same clock same dll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:40
L6	49	algorithmic adj adc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:40
L7	30	adc same sample same hold same sampling adj clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:40
L8	1	phase adj switchable adj capacitors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/01 12:41